ABSTRACT

Methods and apparatus to provide an execution mode transition are disclosed. One example method includes receiving in an operating system runtime environment a firmware code update to be implemented in a multiprocessor system, storing the firmware code update, and issuing an interprocessor interrupt to each processor of the multiprocessor system. The method further includes storing state information for each processor of the multiprocessor system, transitioning from an operating system runtime environment to a pre-operating system environment, and implementing the firmware code update in the pre-operating system environment. The state information is then read and restored to each processor to transition from the pre-operating system environment to an operating system runtime environment.